

07 /24 /03

Sheet 1 of 1

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. 1639.1008D	APPLICATION NO. <i>10/625,734</i> Unassigned
LIST OF REFERENCES CITED BY APPLICANT  (Use several sheets if necessary)		FIRST NAMED INVENTOR Naoto FUJISHIMA			
		FILING DATE July 24, 2003	GROUP ART UNIT <i>2822-2823</i>		

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
K.N.	AA	6,545,316	04-2003	Baliga	257	329	<i>06/23/2000</i>
K.N.	AB	6,495,883	12-2002	Shibata et al.	257	331	<i>02/01/2002</i>
	AC						
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
K.N.	AG	2000-114512	04/21/00	JAPAN			Abstract
K.N.	AH	EP-1094525	04-2001	Europe			
	AL						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

TRANSLATION YES NO
-----------------------

K.N.	AM	Fujishima, Naoto and Salama, C. Andre T., "A Trench Lateral Power MOSFET Using Self-Aligned Trench Bottom Contact Holes", Department of Electrical and Computer Engineering, University of Toronto, Toronto, Ontario, Canada M5S 3G4, 4 Pages, IEEE, 1997.
K.N.	AN	Parthasarathy, V., et al., "A 0.35µm CMOS Based Smart Power Technology For 7V-50V Applications", SmartMOS Technology Center, Semiconductor Products Sector, Motorola Inc., 4 Pages, IEEE, 2000, Catalog Number 00CH37094C.

EXAMINER	DATE CONSIDERED
<i>K. Neung-Jae</i>	12/08/04
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

## LIST OF COPENDING APPLICATIONS

**ATTACHMENT 1(f)**

ATTORNEY DOCKET NO. 1639.1008D	APPLICATION NO. <b>10/625,734</b> Unassigned
FIRST NAMED INVENTOR <b>Naoto FUJISHIMA</b>	
FILING DATE <b>July 24, 2003</b>	GROUP ART UNIT <b>2822 2823</b>

The following, prior-filed, copending U.S. patent application(s) is/are listed in accordance with the duty of disclosure provisions of 37 CFR § 1.56, so that the Examiner may consider same should he deem any thereof to be material to examination of the subject application. Pursuant to 37 CFR 1.98(a)(2)(iii), a copy of the identified copending application(s) is provided.

It is requested that the Examiner acknowledge his consideration of application(s) below-listed by initialing same in the space provided adjacent each such application and that the Examiner sign and date this form at the bottom thereof to confirm such consideration having been given.

This submission in no way represents an admission that any of the information listed herein constitutes prior art with respect to the subject application and unless and until such prior art status is established, this submission is not a request that the information presented herein be printed on the face of any patent issuing from the subject application in which this information is being filed.

<b>U.S. PATENT APPLICATION DOCUMENTS</b>				
EXAMINER INITIAL	U.S. SERIAL NO.	FILING DATE	NAME	ASSIGNEE
K.N.	1 10/156,641	May 29, 2002	Naoto FUJISHIMA	Fuji Electric Co., Ltd.
2				
3				
4				
5				
6				
7				
8				

EXAMINER <i>K. Neumann</i>	DATE CONSIDERED <i>12/08/04</i>
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	